AN1925

Using the Motorola Accelerometer Evaluation Board

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INTRODUCTION

This application note describes the Motorola Accelerometer Evaluation Boards (Figure 1). The Accelerometer Evaluation Boards are small circuit boards intended to serve as aids in system designs with the capability for mounting any of the low g devices: MMA1250D, MMA1260D, and MMA1270D. The evaluation boards are useful for quickly evaluating any of these three devices. They also provide a means for understanding the best mounting position and location of an accelerometer in your product.

CIRCUIT DESCRIPTION

Figure 2 is a circuit schematic of the evaluation board. The recommended decoupling capacitor at the power source and recommended RC filter at the output, are included on the evaluation board. This RC filter at the output of the accelerometer minimizes clock noise that may be present from the switched capacitor filter circuit. No additional components are necessary to use the evaluation board.

Refer to the respective datasheet of the device being used for specifications and technical operation of the accelerometer. The evaluation board has a 4-pin header (J1 in Figure 1) for interfacing to a 5 volt power source or a 9 to 15 volt power source (for example, 9 V battery). Jumper JP1 (see Figure 1) must have the following placement: on PS if a 5 V supply is being used or on BATT if a 9 V to 15 V supply is used. A 5 V regulator (U1 in figure 1) supplies the necessary power for the accelerometer in the BATT option.

The power header also provides a means for connecting to the accelerometer analog output through a wire to another breadboard or system. Four test points are included to allow access to the following signals: V_{DD}, GND, ST and STATUS.

The ON/OFF switch (S1) provides power to the accelerometer and helps preserve battery life if a battery is being used as the power source. S1 must be set towards the "ON" position for the accelerometer to function. The green LED (D1) is lit when power is supplied to the accelerometer.

A self-test pushbutton (S2) on the evaluation board is a self-test feature that provides verification of the mechanical and electrical integrity of the accelerometer. The **STATUS** pin is an output from the fault latch, and is set high if one of the fault conditions exists. A second pressing of the pushbutton (S2) resets the fault latch, unless of course one or more fault conditions continue to exist.

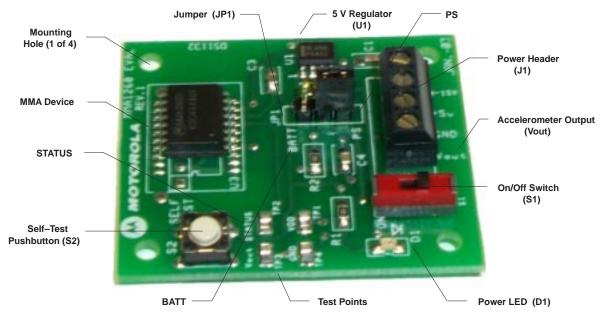


Figure 1. Motorola Accelerometer Evaluation Board





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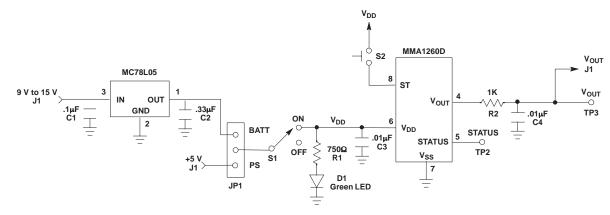


Figure 2. Evaluation Board Circuit Schematic

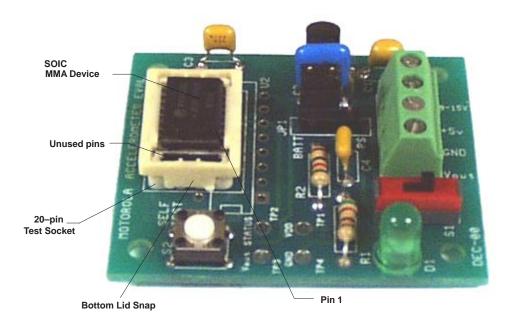


Figure 3. Motorola Accelerometer Evaluation Board with Test Socket

The board allows for direct mounting of a 16–pin SOIC package or a 20–pin socket. A 20–pin test socket is used to allow for evaluation of more than one device without soldering directly to the board and potentially damaging the PCB. Care must be taken in placing the device correctly in the socket, as four pins of the socket will not be used. With the board oriented as shown in Figure 3, Pin 1 should face downward and the device should be positioned toward the top of the test socket, thereby exposing the bottom four pins of the test socket. The socket is marked to help identify the 4 unused socket pins. Lids to secure the device in the socket are included with the board and delicately snap into place. The lids can be removed

by applying pressure to the sides of the lid or by lifting the top and bottom snaps of the lid.

Table 1. Pin Out Description

Pin	Name	Description	
4	V _{OUT}	Output voltage of the accelerometer	
5	STATUS	Logic output pin to indicate fault	
6	V _{DD}	Power supply input	
7	V _{SS}	Power supply ground	
8	ST	Logic input pin to initiate self-test	

Freescale Semiconductor, Inc.

BOARD LAYOUT AND CONTENT

Figures 4 and 5 show the layout used on the evaluation board. Surface mount components have been selected to save board space and to reduce costs.

MOUNTING CONSIDERATIONS

System design and sensor mounting can affect the response of a sensor system. The placement of the sensor itself is critical to obtaining the desired measurements. It is important that the sensor be mounted as rigidly as possible to obtain accurate results. Since the thickness and mounting of the board varies, parasitic resonance may distort the sensor measurement. Hence, it is vital to fasten and secure to the largest mass structure of the system, i.e. the largest truss, the largest mass, at the point closest to the source of vibration. On the other hand, dampening of the sensor device can absorb much of the vibration and give false readings as well. The evaluation board has holes on the four corners of the board for

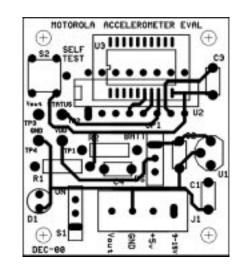


Figure 4. Board Layout (Component Side)

ORDERING INFORMATION

The Motorola Accelerometer Evaluation Boards are available to order as Evaluation Kits. Each kit includes an Evaluation Board with the accelerometer in an anti–static bag, a sensor repository CD that contains sensor brochures, data sheets, and the revised data book, and instructions for using the Evaluation Board.

Table 2 shows the kits that are available, or will be available soon. Check our website for availability.

Table 2. Evaluation	Nits Ord	er Information
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Part Number	G Range	Axis
Kit1925MMA1250D	5.0g	Z
Kit1925MMA1260D	1.5g	Z
Kit1925MMA1270D	2.5g	Z
Kit1925MMA2260D	1.5g	Х

mounting. It is important to maintain a secure mounting scheme to capture the true motion.

Orientation of the sensor is also crucial. For best results, align the sensitive axis of the accelerometer to the axis of vibration. In the case of the MMA1260D, a Z-access device, the sensitive axis is perpendicular to the plane of the evaluation board.

SUMMARY

The Accelerometer Evaluation Boards are design-in tools for customers seeking to quickly evaluate an accelerometer in terms of output signal, device orientation, and mounting considerations. With the battery supply option and corner perforations, the board can easily be mounted on the end product; such as a motor or a piece of equipment. Easy access to the main pins allows for effortless interfacing with a microcontroller or other system electronics. The simplicity of this evaluation board provides reduced development time and assists in selecting the best accelerometer for the application.

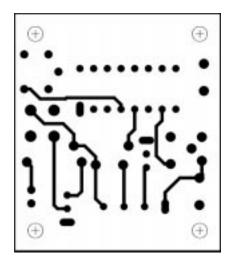


Figure 5. Board Layout (Back Side)

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